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(54) **Random number's seed generating circuit, driver having the same, and SD memory card system**

(57) A random number's seed is changed without using any nonvolatile memory every time the circuit is activated. An oscillator (14) operates immediately after the power supply is turned on to generate a high-speed clock. A counter (15) operates in accordance with the high-speed clock to change the count value at a high speed. When a sufficient time has elapsed after the power supply is turned, a power-on reset circuit (10) out-

puts a power-on reset signal indicating that the internal power supply is stabilized. The power-on reset signal is sufficiently slower than the high-speed clock. The timing at which the power-on reset signal is input to a latch circuit (16) varies. Hence, the count value (random number's seed) latched by the latch circuit (16) changes every time the power supply is turned on. A random number is generated using the random number's seed.

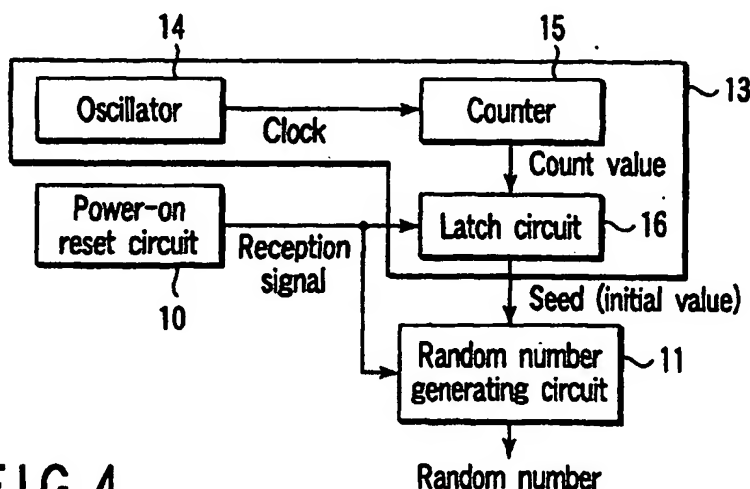


FIG. 4



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Office

EUROPEAN SEARCH REPORT

Application Number
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The present search report has been drawn up for all claims			
Place of search The Hague		Date of completion of the search 30 November 2004	Examiner Cohen, B
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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